

Fig. 1a

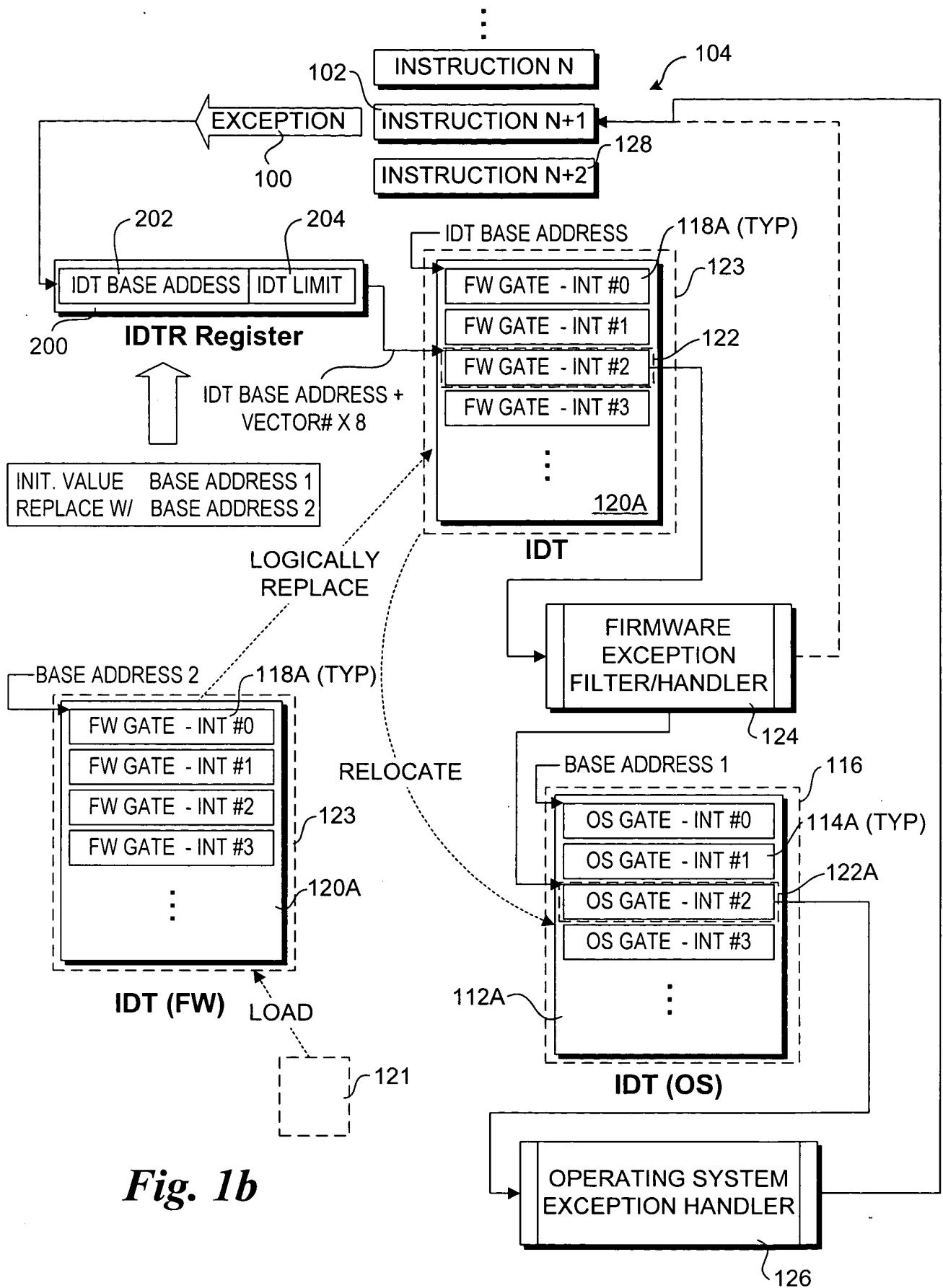


Fig. 1b

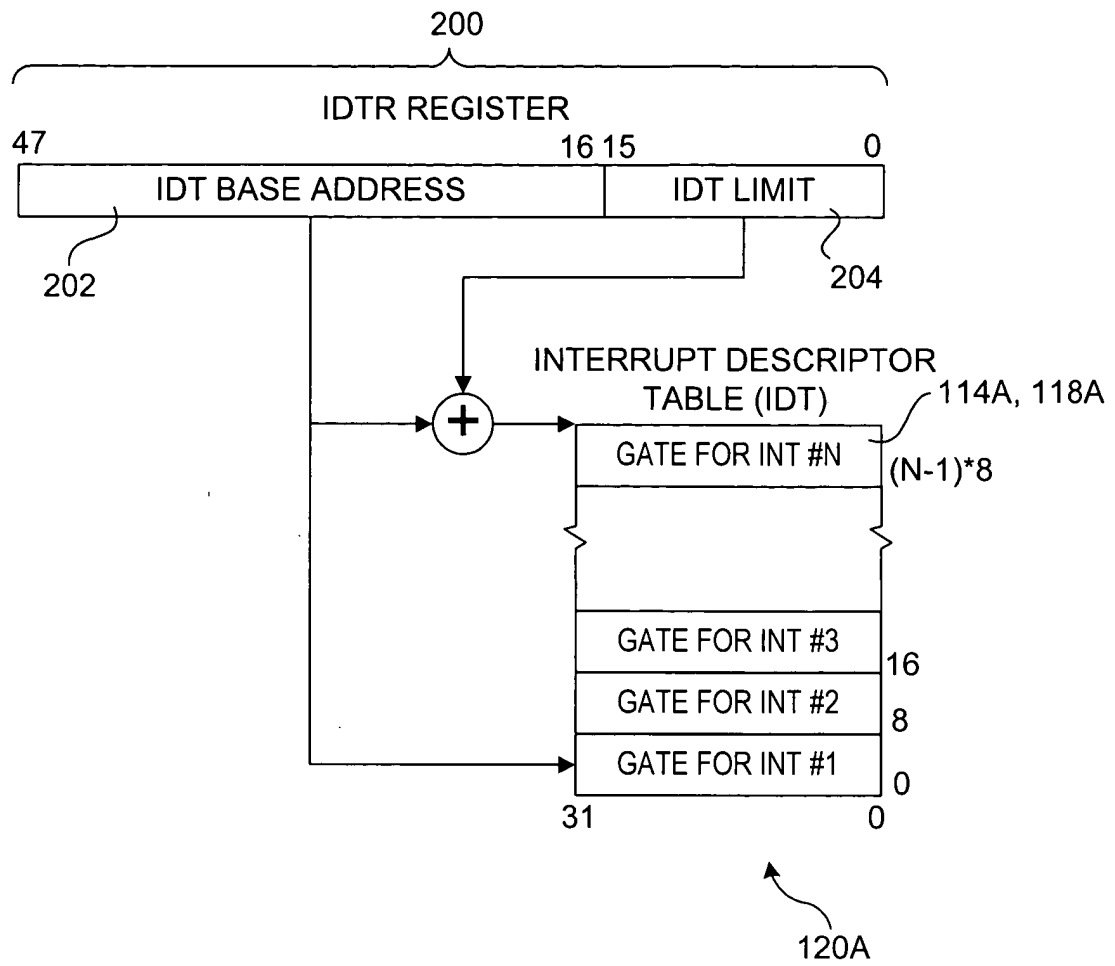
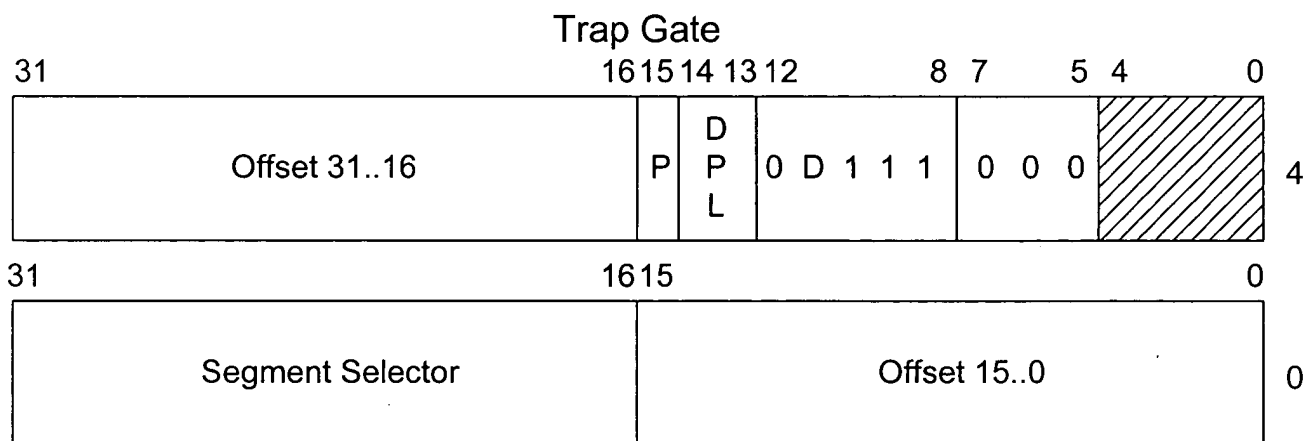
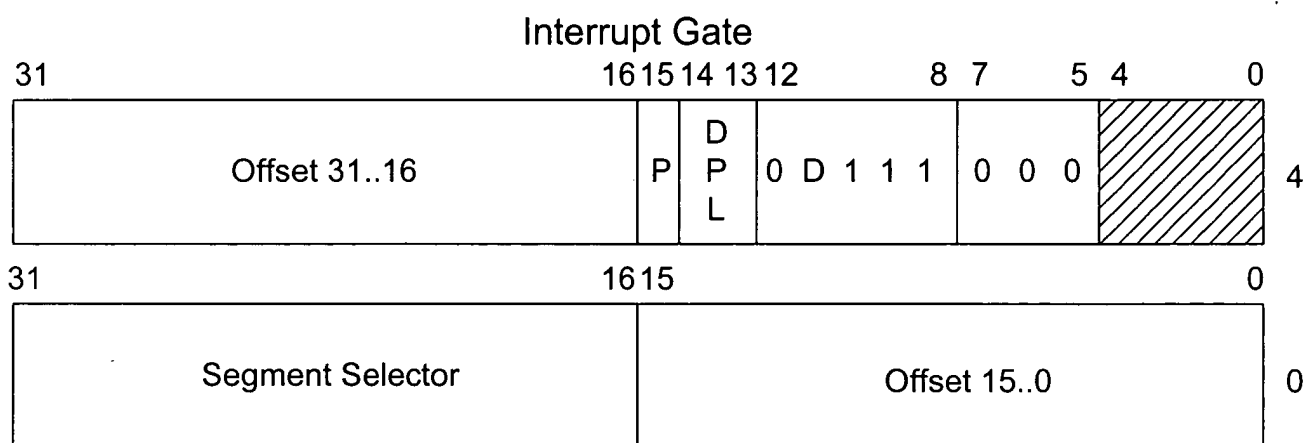
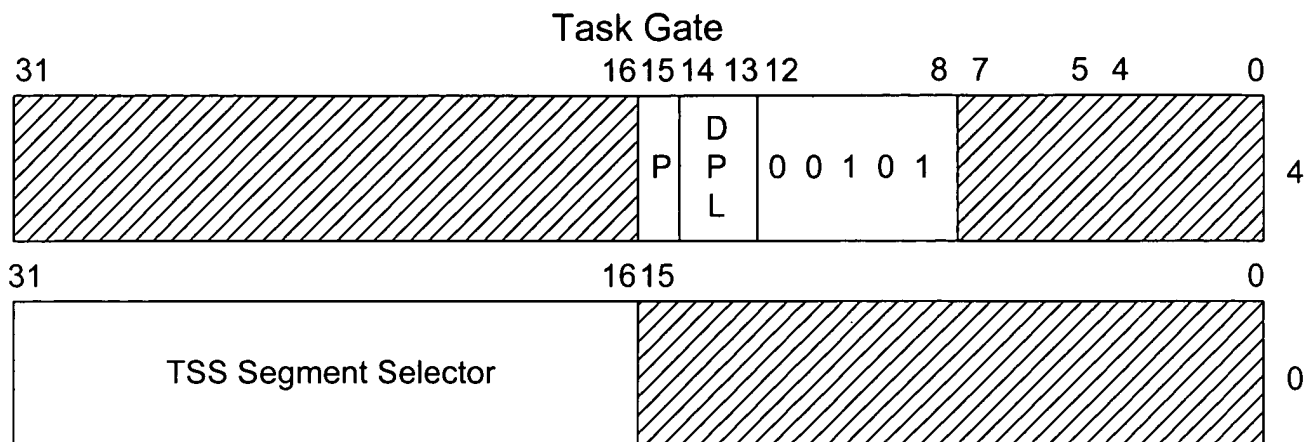


Fig. 2



DPL	Descriptor Privilege Level
Offset	Offset to procedure entry point
P	Segment Present flag
Selector	Segment Selector for destination code segment
D	Size of gate: 1 = 32 bits; 0 = 16 bits

Reserved

Fig. 3

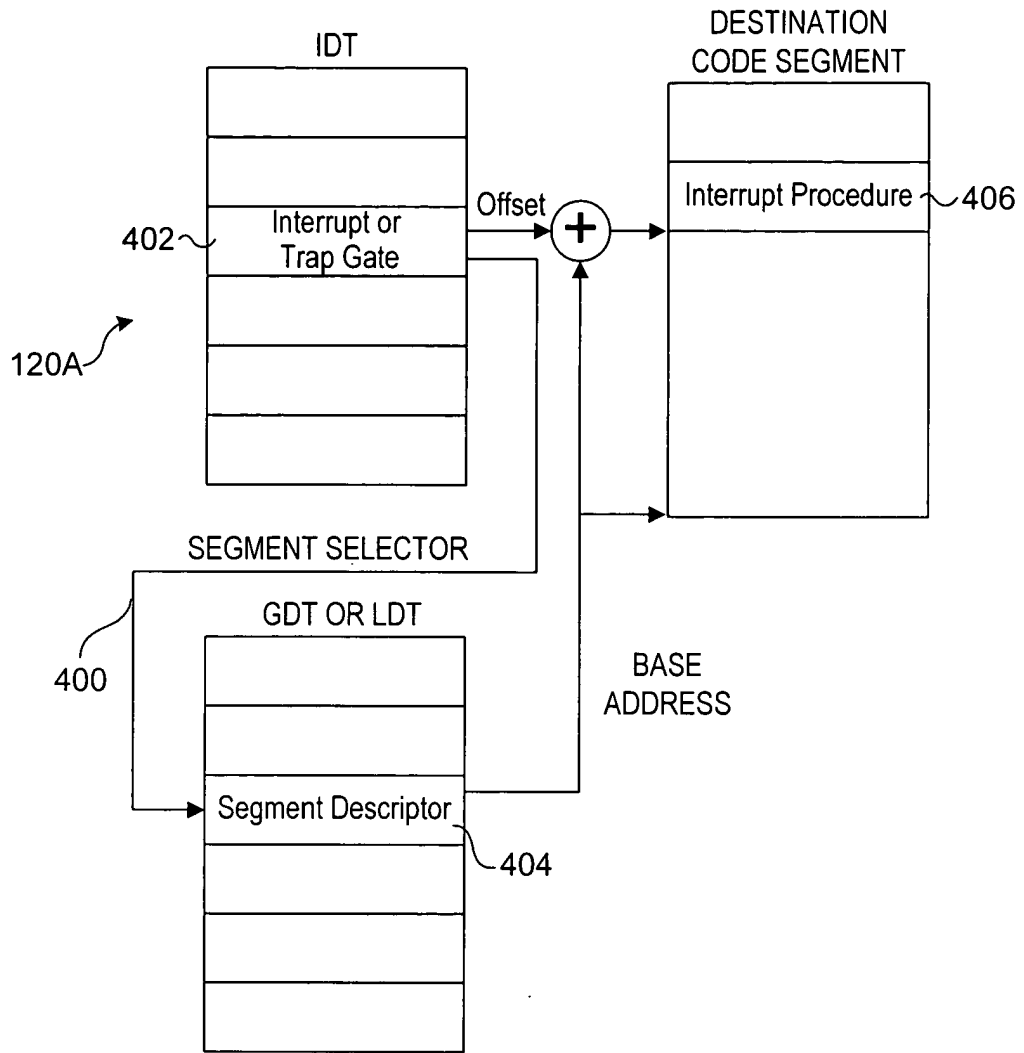


Fig. 4

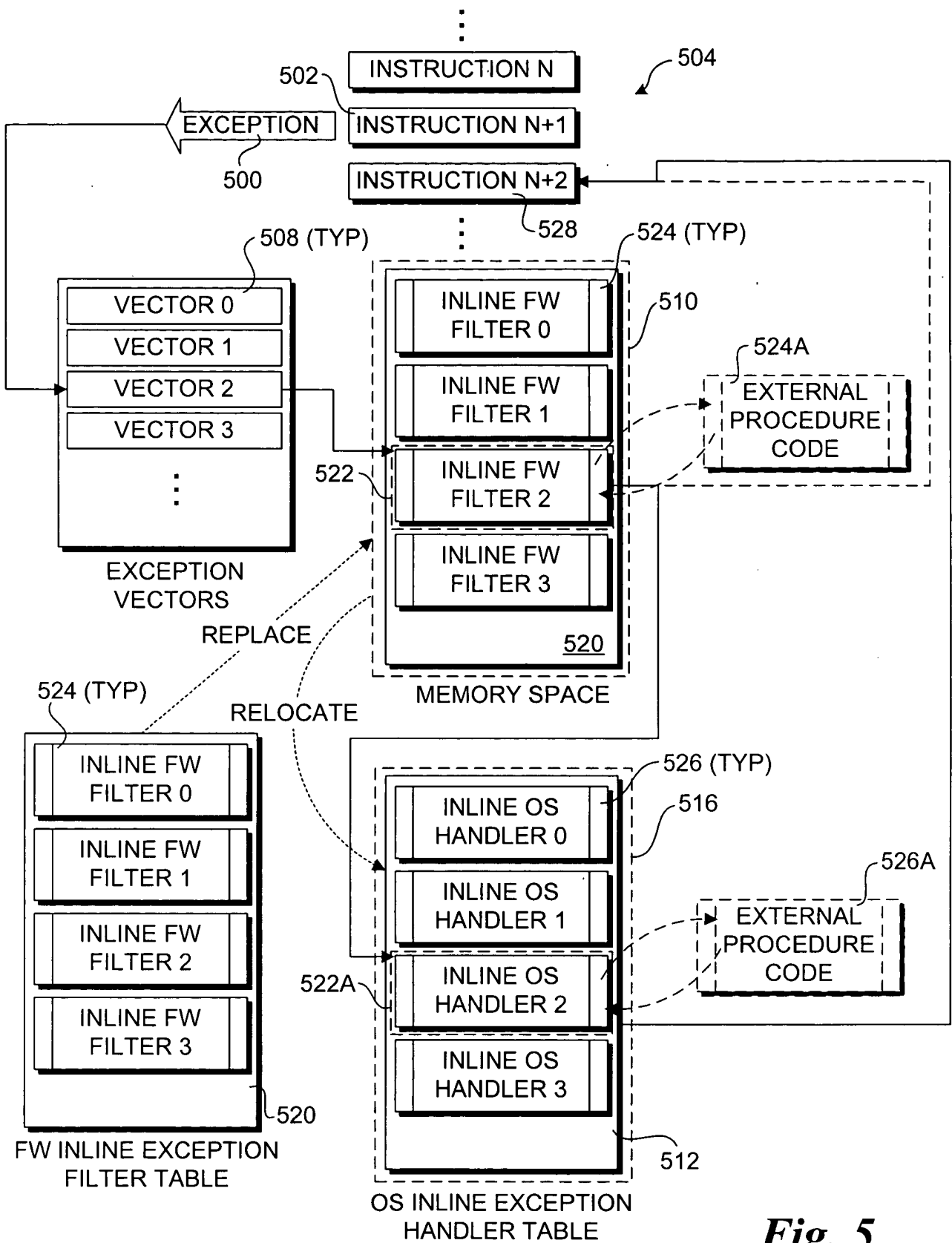


Fig. 5

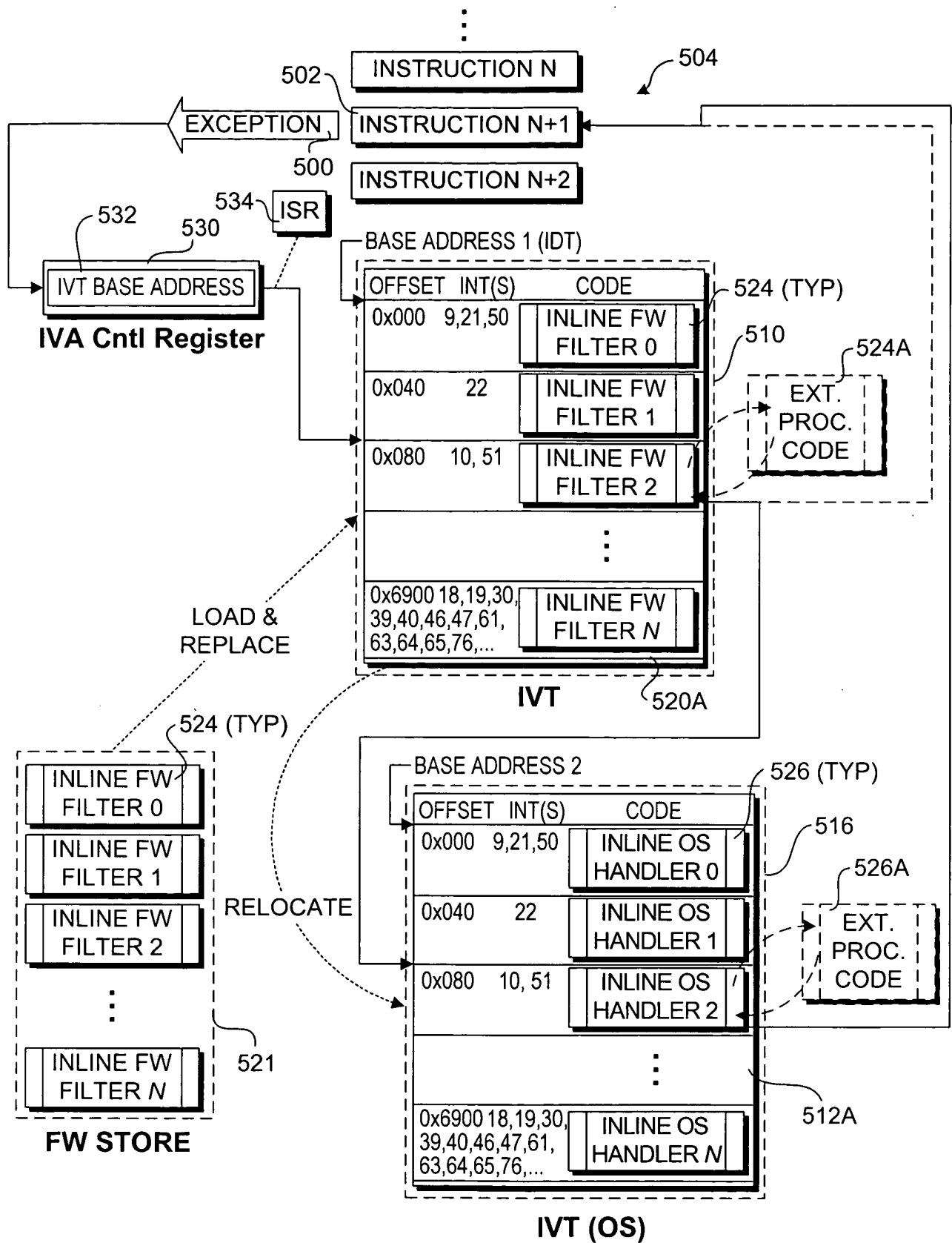


Fig. 5a

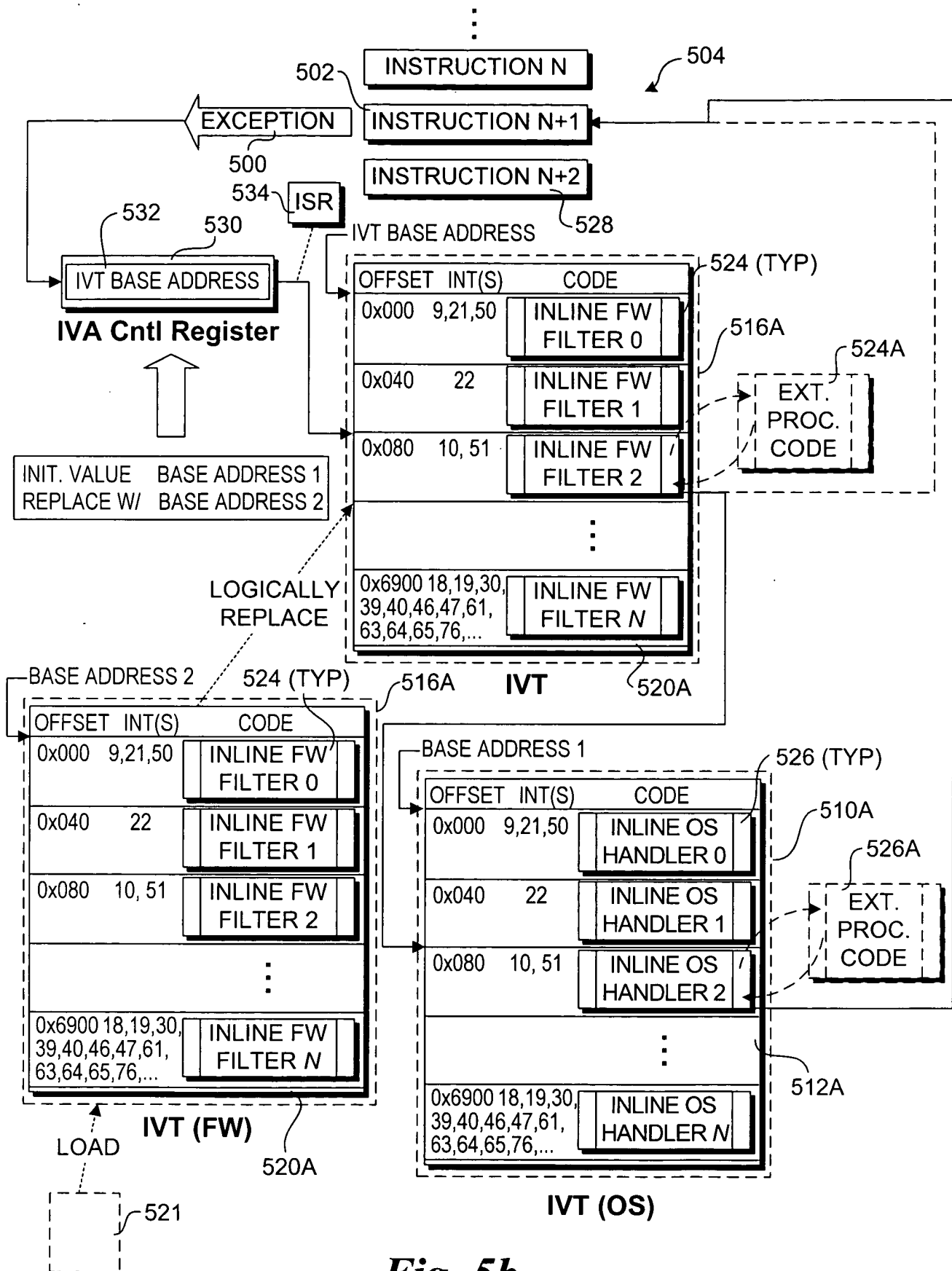


Fig. 5b

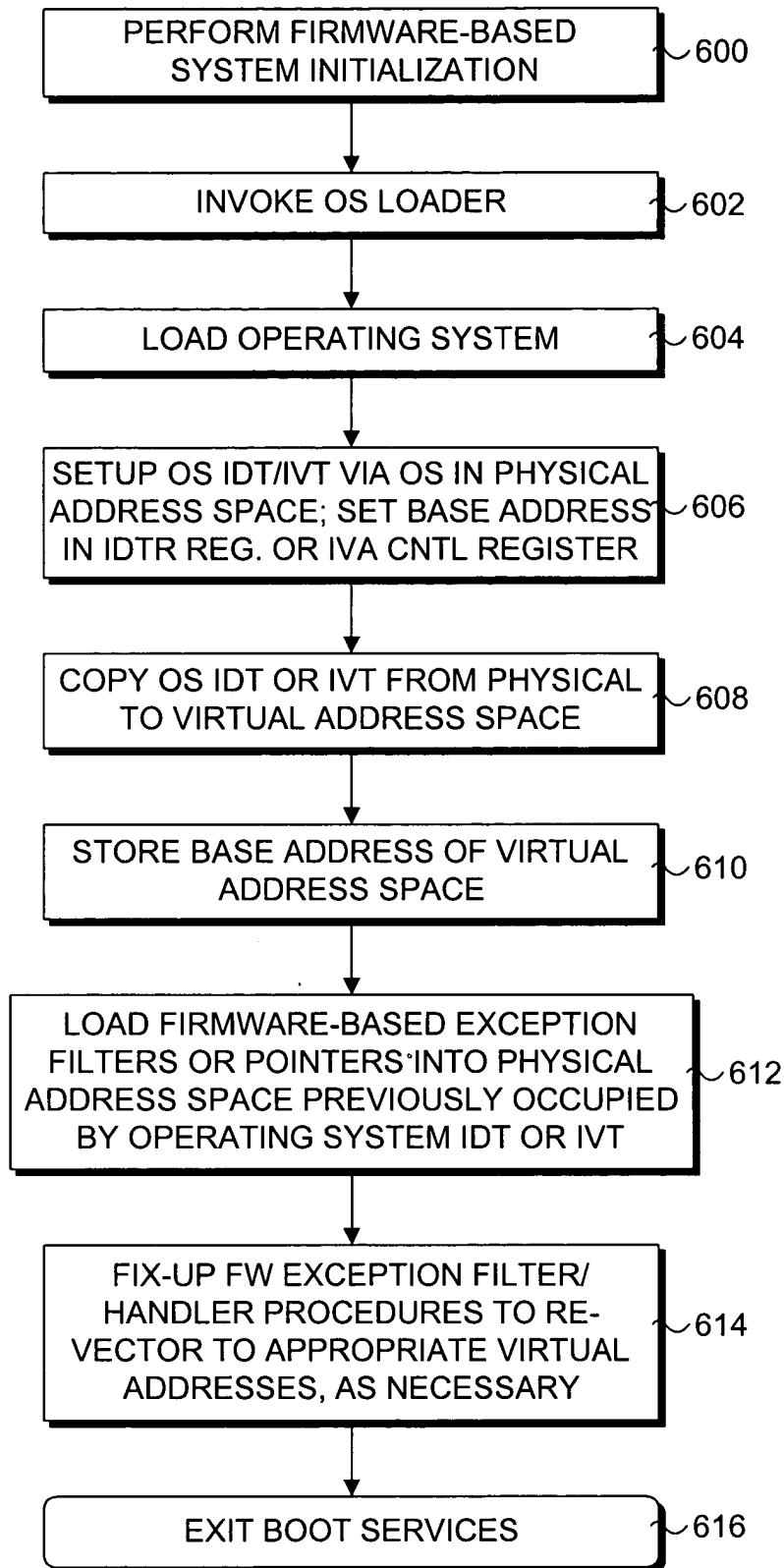


Fig. 6a

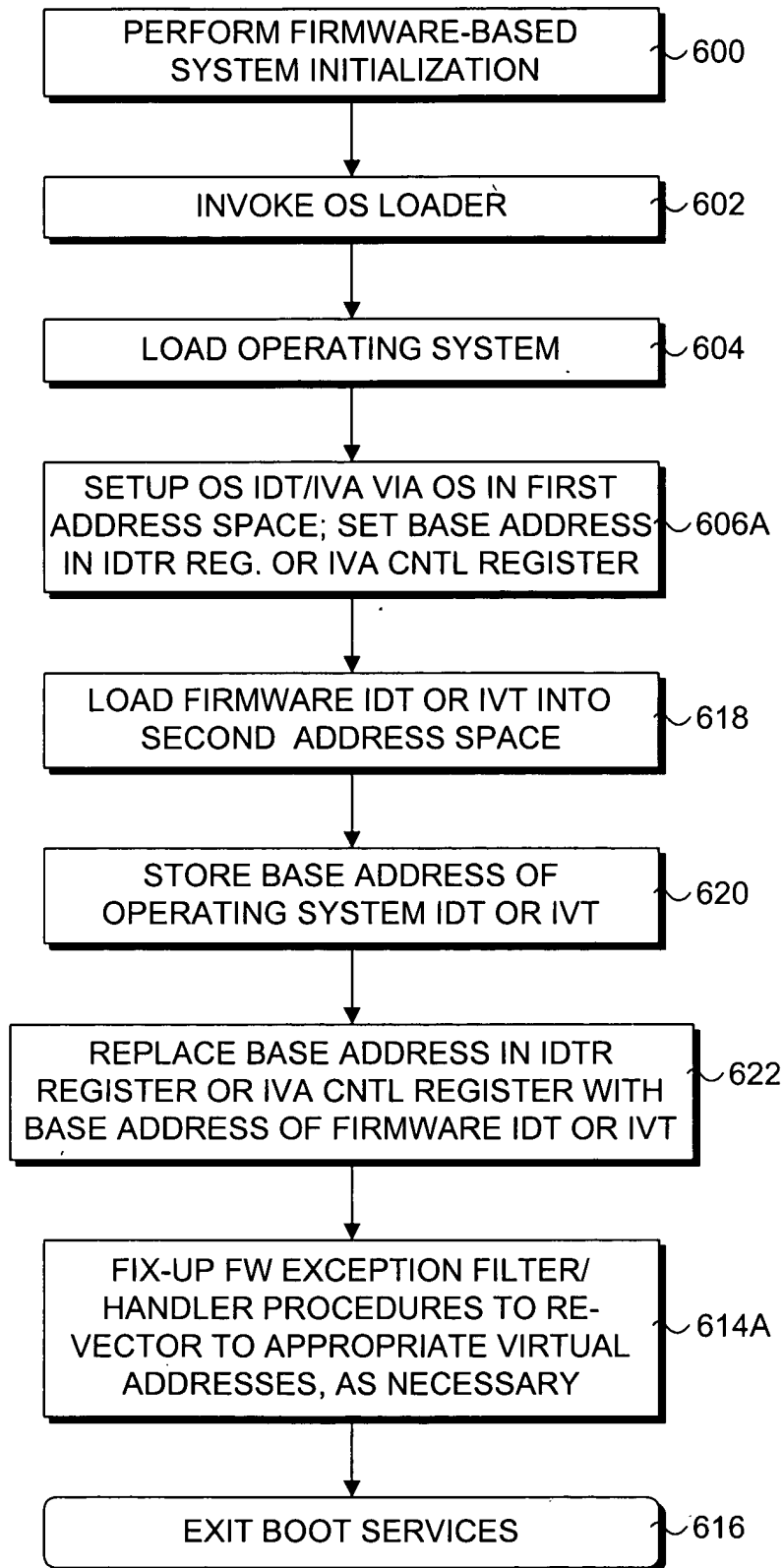


Fig. 6b

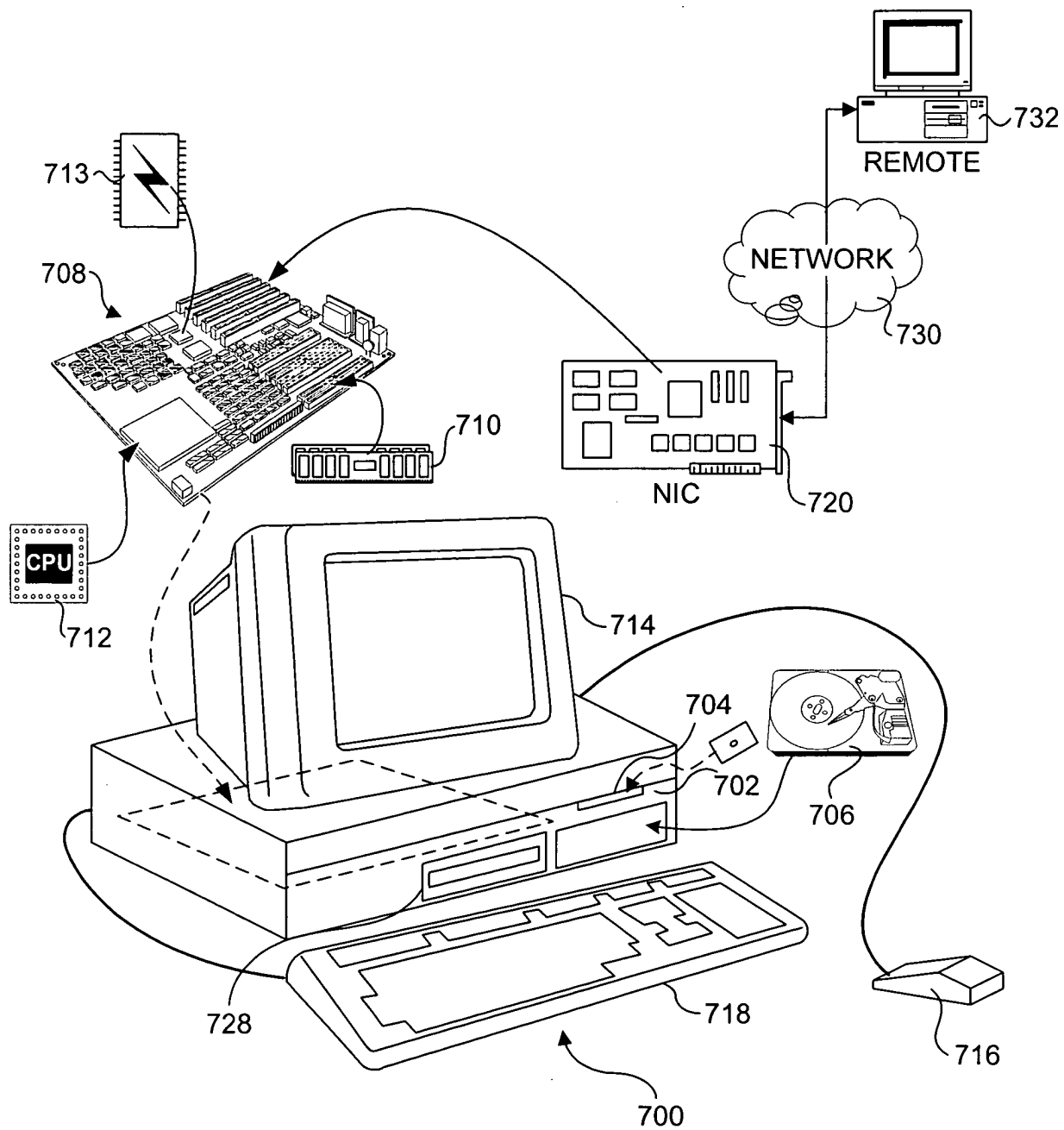


Fig. 7